

FIG. 1

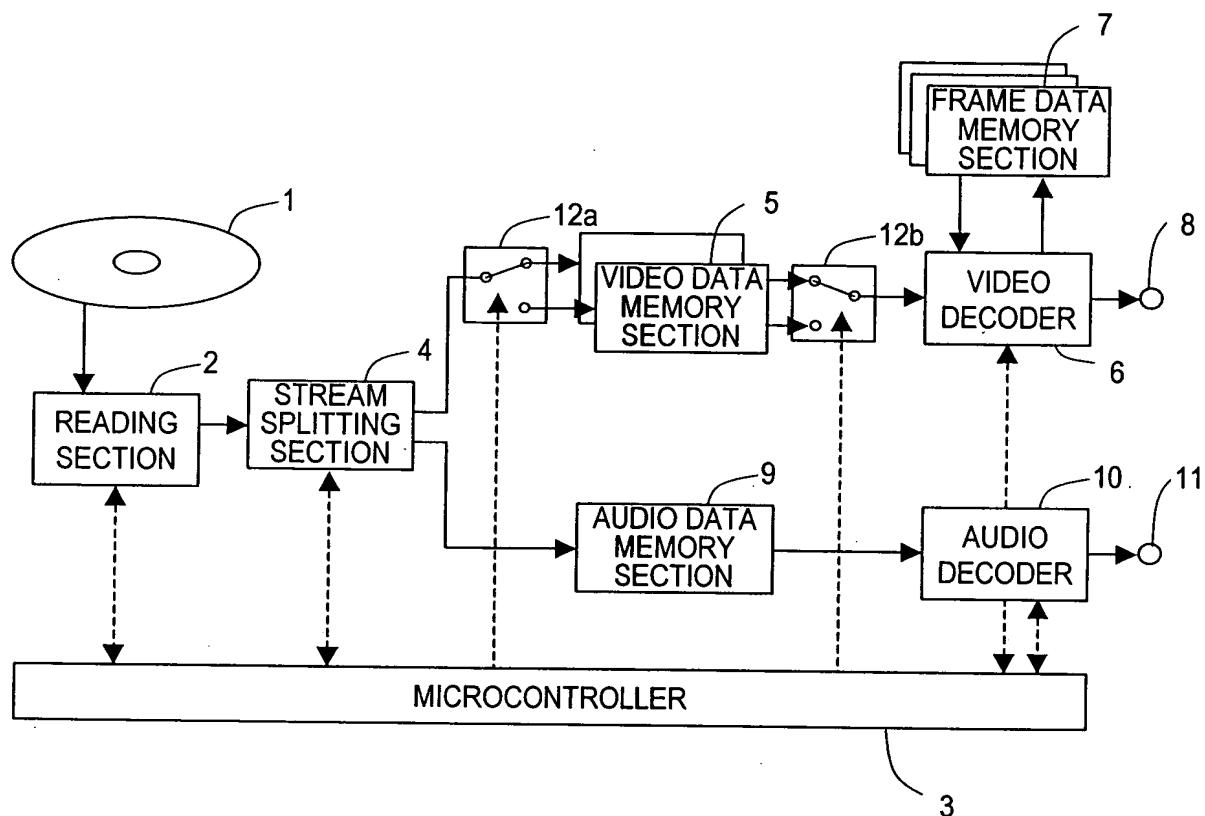


FIG.2

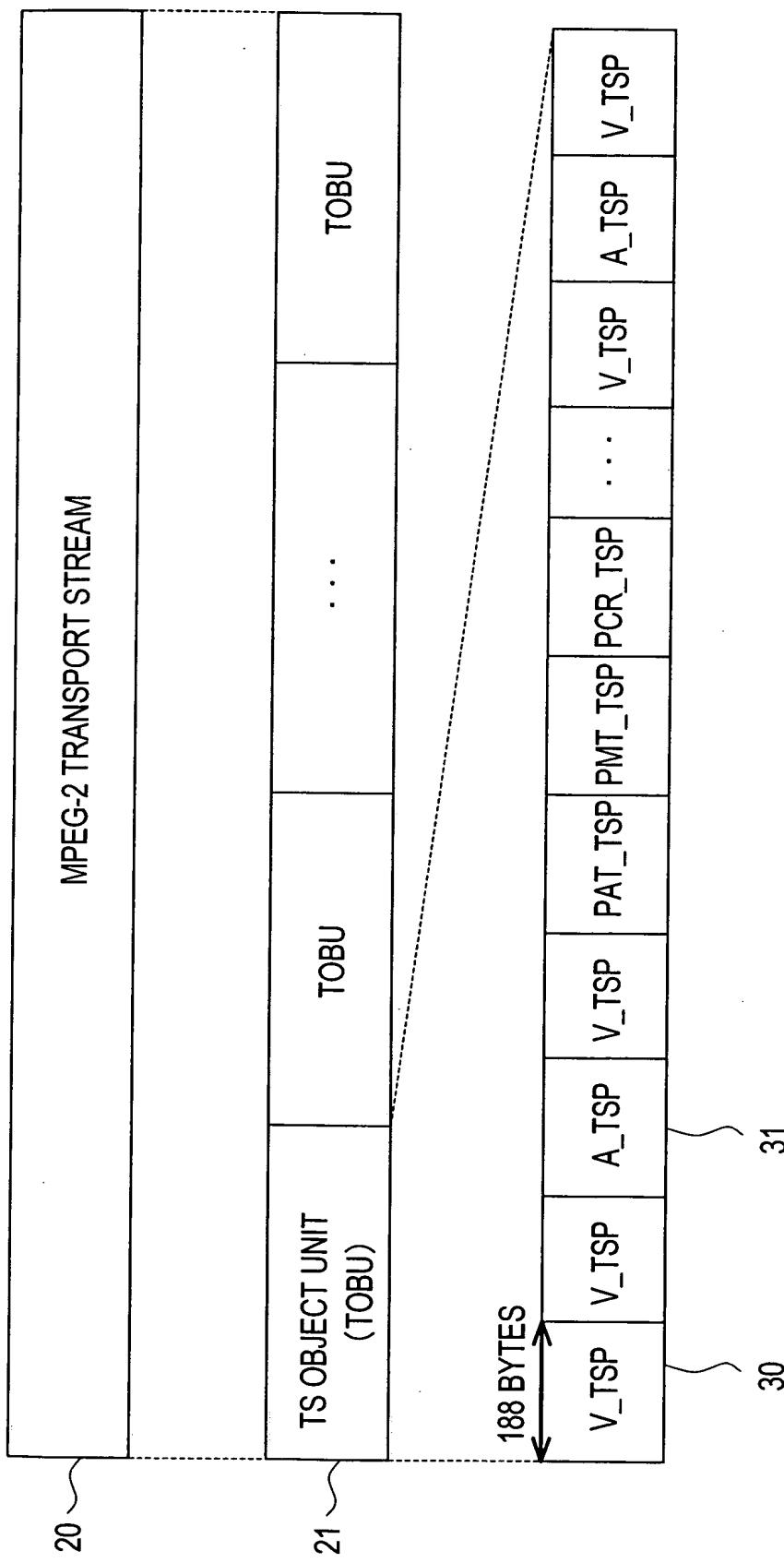
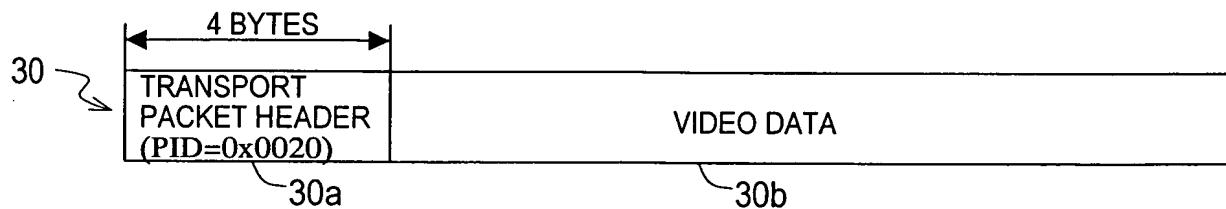


FIG.3

(a) V_TSP



(b) A_TSP

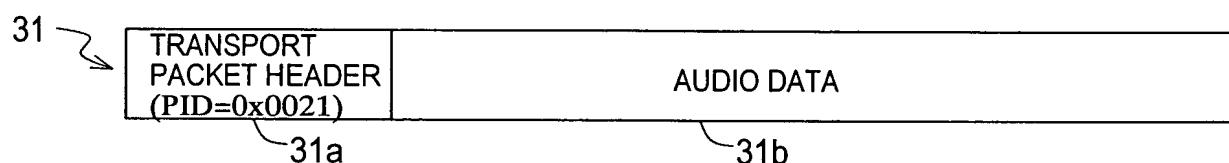


FIG. 4

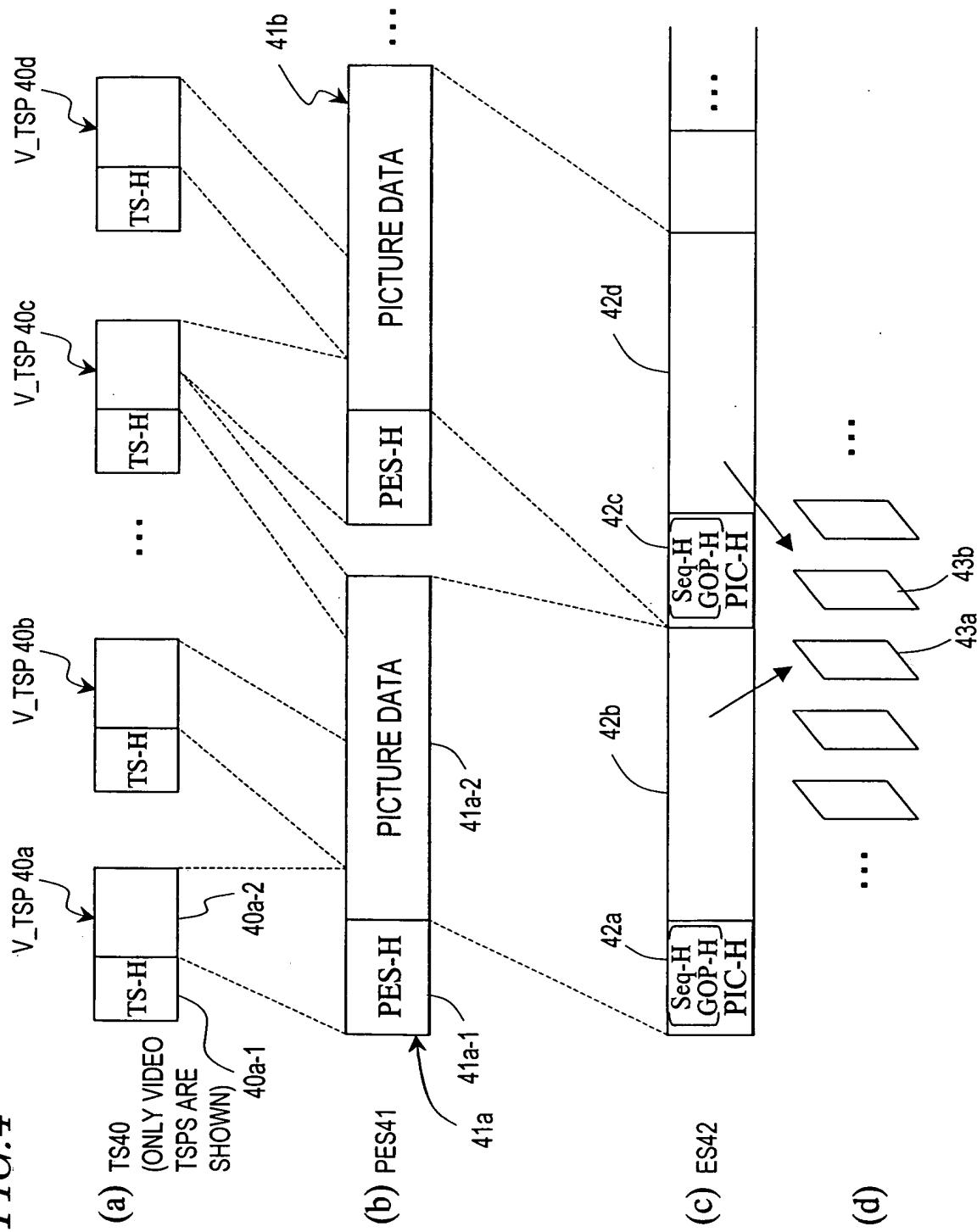


FIG. 5

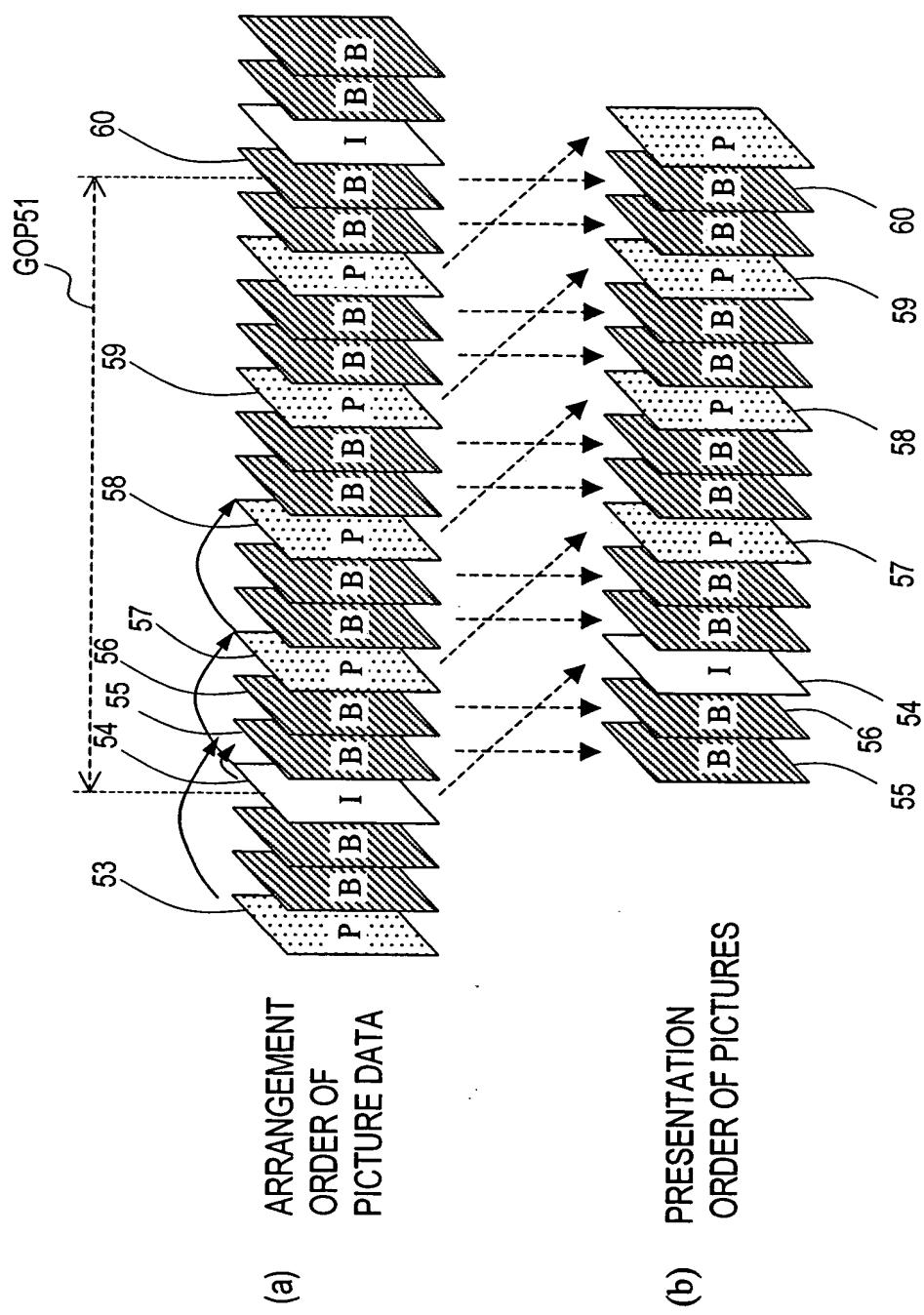


FIG.6

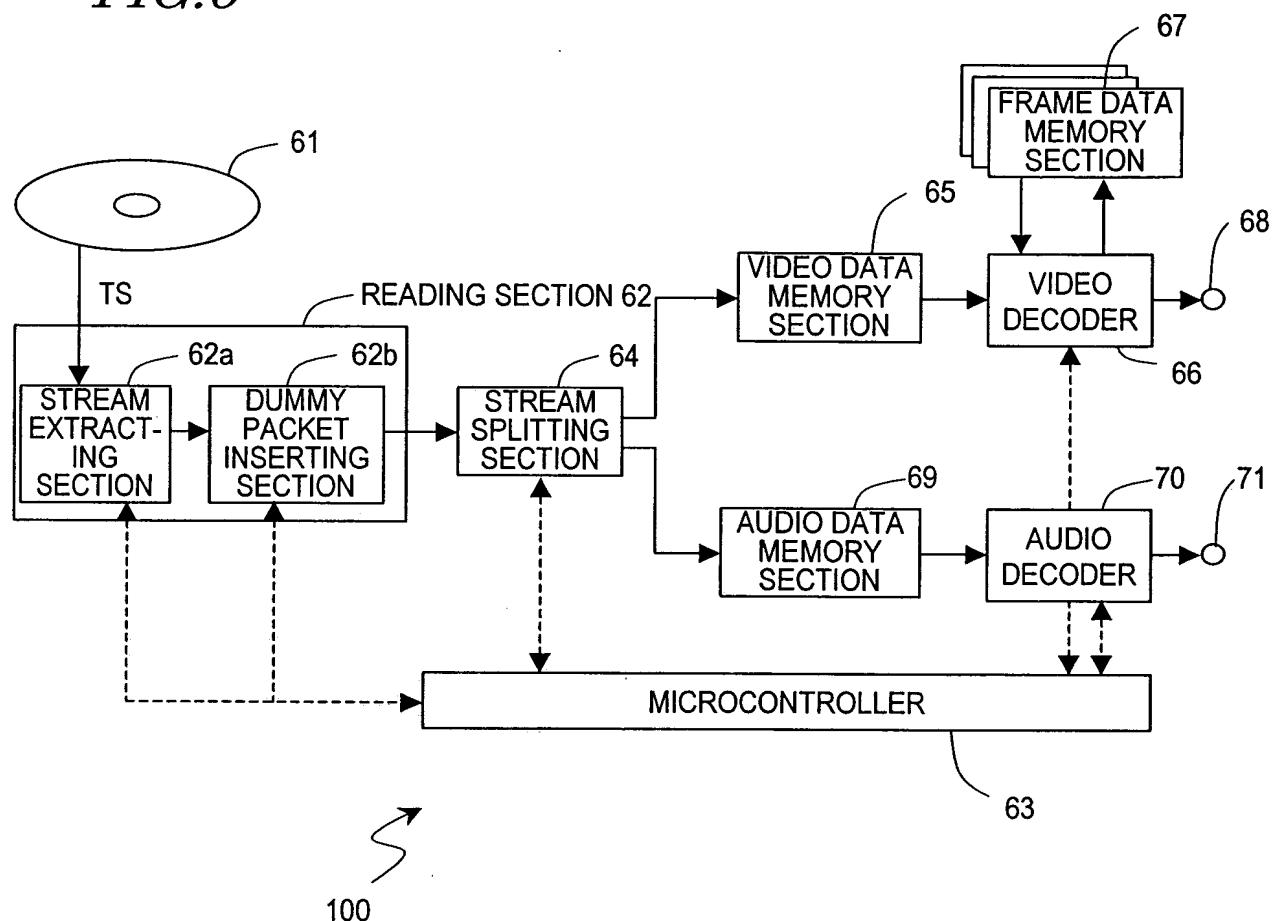


FIG. 7
PACKET HEADER 72a
(DUMMY PID=0 x1FFF)

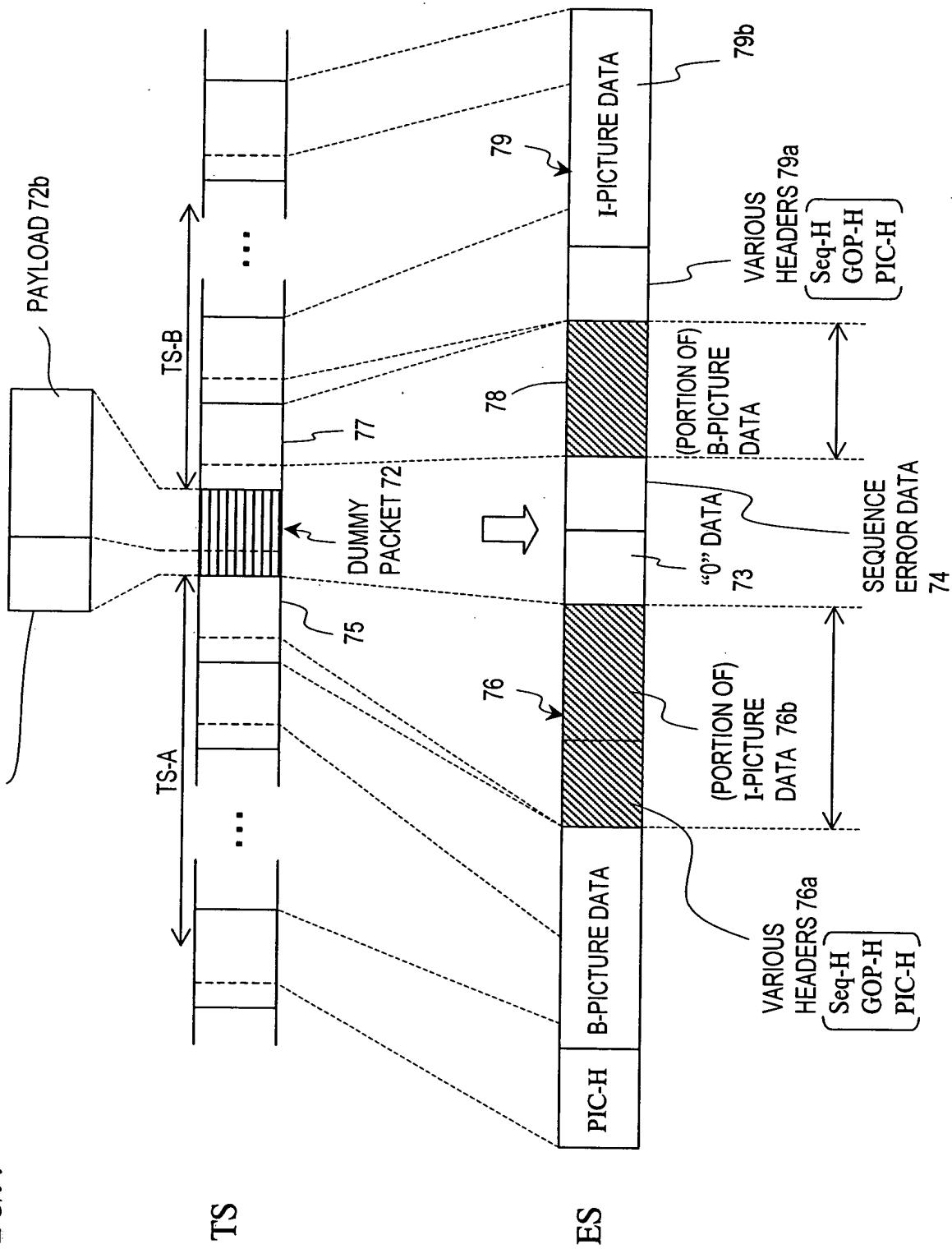


FIG. 8

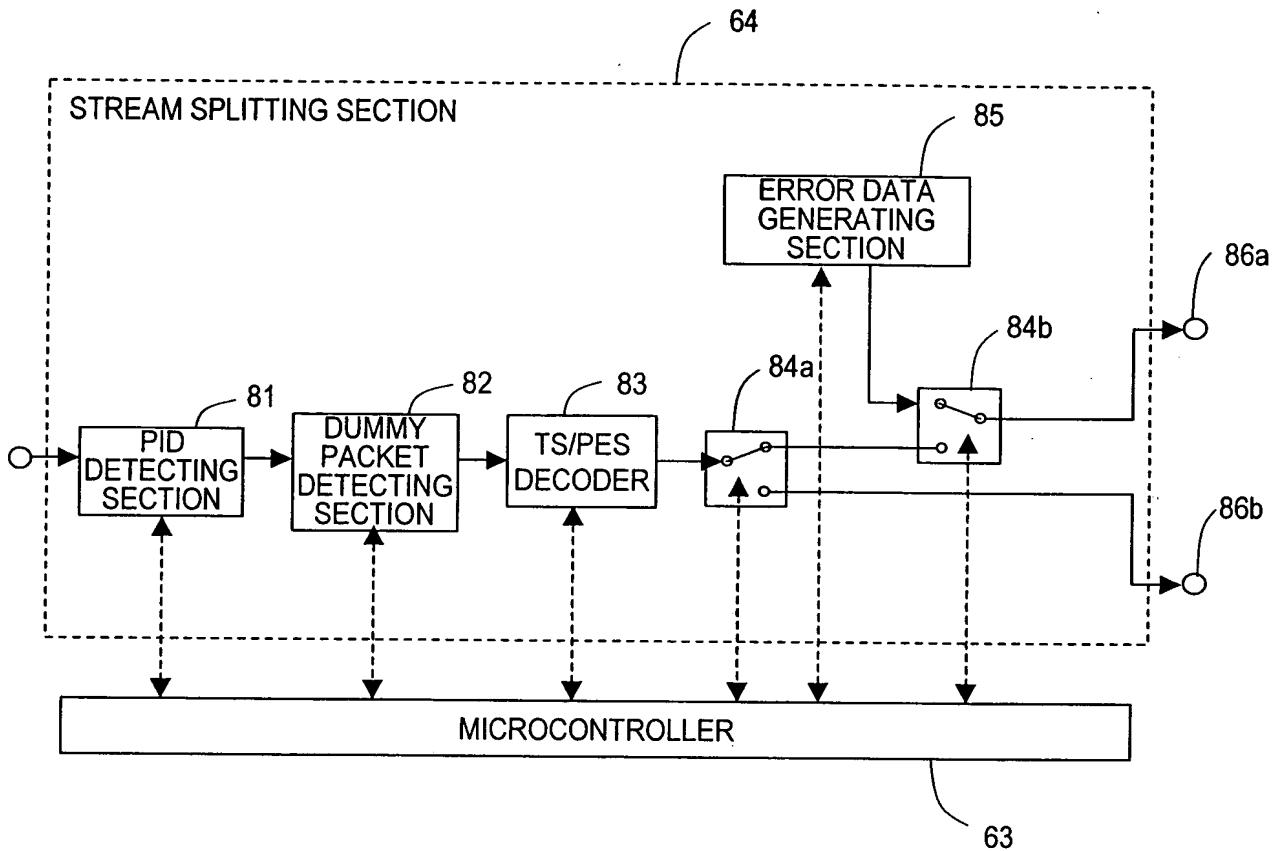


FIG.9

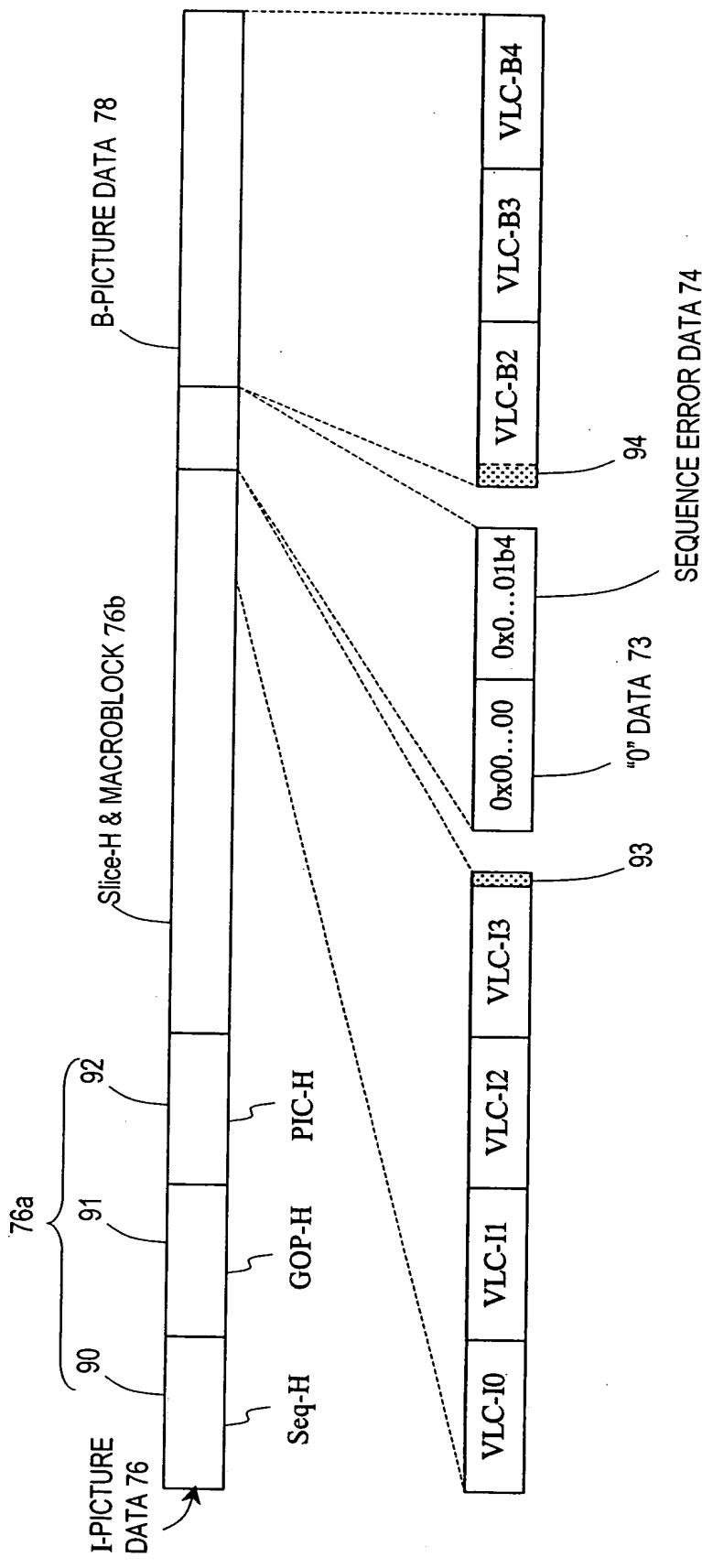


FIG.10

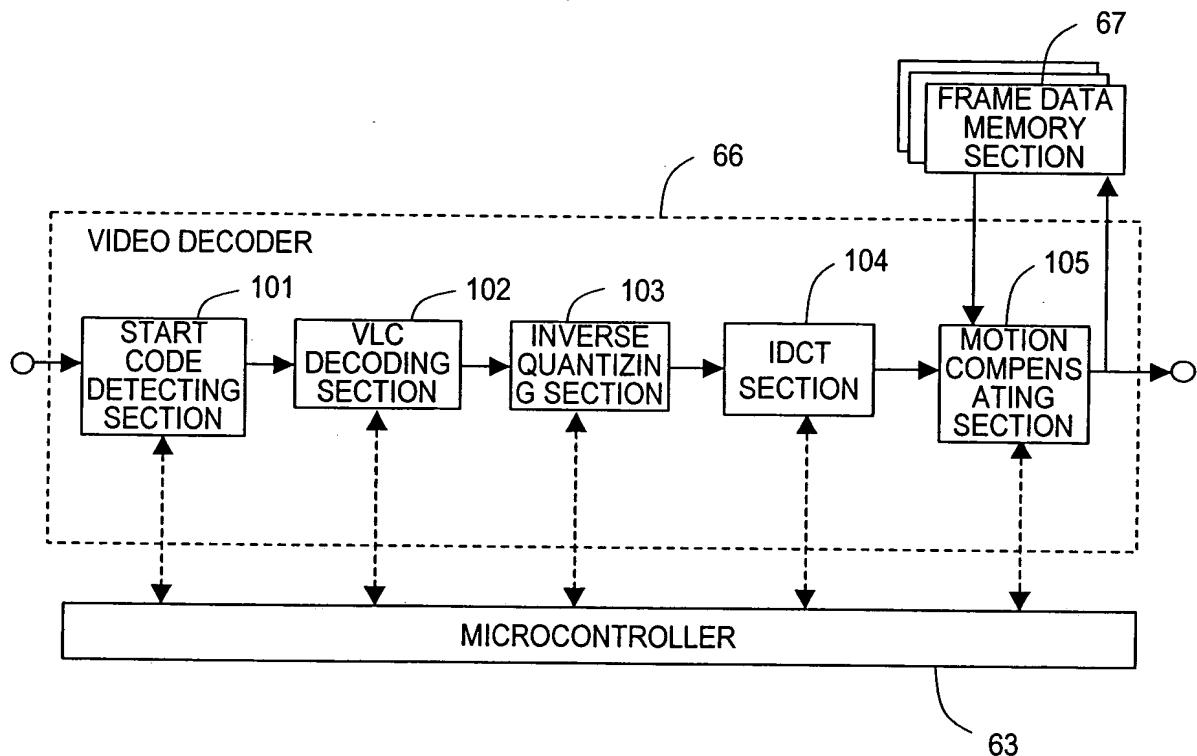


FIG.11

